

## Gary J. Nutt

### Ph.D. Students

1. Lin, Wang-ting, "Robust QoS Scheduler in the Open Real-time Systems," August, 2009.
2. Steinke, Robert C., "Consistency Model Transitions for Shared Memory," May, 2001.
3. Siewert, Sam, "A Real-Time Execution Performance Agent Interface for Confidence-Based Scheduling," December, 2000.
4. Adam Jonathon Griff, "Gryphon: A Dynamically Tailorable Mechanism for Customizing Location and Caching Policies in Distributed Object Subsystem," May, 2000.
5. Scott A. Brandt, "Soft Real-Time Processing with Dynamic QoS Level Resource Management," August, 1999.
6. Richard L. Blumenthal, "Supporting Unstructured Activities with a Meta-Contextual Protocol in Situation-Based Workflow," May, 1998.
7. Zulah K. F. Eckert, "Trace Extrapolation for Parallel Programs on Shared Memory Multiprocessors," May, 1995.
8. Jeffrey D. McWhirter, "Characterization, Specification and Generation of Visual Language Applications," May, 1995.
9. Adam L. Beguelin, "Deterministic Parallel Programming in Phred," May, 1990.
10. Isabelle M. Demeure, "A Model, ParaDiGM, and a Software Tool, VISA for the Representation, Design and Simulation of Parallel, Distributed Computations", August, 1989.

### M.S. Thesis Students

1. Paul Griepentrog, "Network Enhancements to the Dynamic QoS Manger," 1999.
2. James Grosvenor Garnett, "Distributed Phase and Frequency Synchronization," 1999.
3. Shahzad Bhatti, "A 3-D Graphics Tool for Software Engineering," 1998.
4. Paul J. Hamill III, "Internet Structure Visualization," (ECE), 1998.
5. William D. Anderson, "An Empirical Study of Optimizations on the CM-5," 1990.
6. Kimbal S. Smith, "An Interface for Interprocess Communication and Control with the MAP Architecture", 1979.
7. Gregory A. Smith, "Input/Output as Process Communication: A Method of Evaluation", 1978.
8. Bruce W. Sanders, "The Design and Simulation of a Bit Slice Implementation for MAP", 1978.
9. Robert T. Krivacic, "The Refinement and Implementation of a Simulation System", 1978.
10. Heinrich Siegman, "Design and Simulation of a Main Memory-Control Units Interface for the Multi Associative Processor System", 1976.
11. Thomas Dodge, 1976 (jointly supervised with C. A. Ellis).
12. Janis P. Osterweil, "A Deadlock Model Based on Process-Resource Actions", 1975.